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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/567,759

02/10/2006

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EXAMINER

HA, NGUYEN T

ART UNIT

PAPER NUMBER

2831

MAIL DATE

DELIVERY MODE

04/14/2008

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b> 10/567,759	<b>Applicant(s)</b> NAITO ET AL.	
	<b>Examiner</b> NGUYEN T. HA	<b>Art Unit</b> 2831	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 10 February 2006.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-21 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 19 is/are allowed.
- 6) ☒ Claim(s) 1,3,20 and 21 is/are rejected.
- 7) ☒ Claim(s) 2 and 4-18 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☒ All    b) ☐ Some \*    c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | 5) <input type="checkbox"/> Notice of Informal Patent Application                       |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br>Paper No(s)/Mail Date <u>0206 &amp; 0406</u> | 6) <input type="checkbox"/> Other: _____  |

## **DETAILED ACTION**

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 3, and 20-21 are rejected under 35 U.S.C. 102(e) as being anticipated by Heusmann et al. (US 7,190,571).

Regarding claim 1, Heusmann et al. disclose a resin molded chip solid electrolytic capacitor comprising a plurality of solid electrolyte capacitor elements (1, 2) horizontally laid in parallel with no gap on a pair of oppositely disposed end parts of a lead frame (3), and a fixing layer (6) which is extending across the plurality of capacitor elements and fixing the capacitor elements with each other (figures 1-2).

Regarding claim 3, Heusmann et al. disclose the fixing layer is a resin layer or an electrically conducting layer (figures 1-2).

Regarding claim 20, it is inherent that the Heusmann et al. capacitor can be used in an electronic circuit.

Regarding claim 21, it is inherent that the Heusmann et al. capacitor can be used in an electronic device.

### ***Allowable Subject Matter***

Claims 2 and 4-18 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Regarding claims 2, and 4-18, the prior art alone or in combination does not teach the limitation of the solid electrolyte capacitor element is produced by stacking an oxide dielectric film layer, a semiconductor layer and an electrically conducting layer in this order to form a cathode part on a surface exclusive of the anode part at one end of an anode substrate comprising a sintered body of a valve-acting metal or an electrically conducting oxide or comprising the sintered body connected with a metal wire.

Claim 19 is allowed.

The following is an examiner's statement of reasons for allowance:

With respect to claim 19, the prior art alone or in combination does not teach the limitation of a method for producing a chip solid electrolyte capacitor comprising the solid electrolyte capacitor element is produced by stacking an oxide dielectric film layer, a semiconductor layer and an electrically conducting layer in this order to form a cathode part on a surface exclusive of the anode part at one end of an anode substrate comprising a sintered body of a valve-acting metal or an electrically conducting oxide or comprising the sintered body connected with a metal wire.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### **Citation Relevant of Prior Art**

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- a. Kida et al. (US 6,816,358) disclose chip type solid electrolytic capacitor.
- b. Arai et al. (US 6,912,117) disclose solid electrolytic capacitor and method of manufacturing the same.

### **Conclusion**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to NGUYEN T. HA whose telephone number is (571)272-1974. The examiner can normally be reached on Monday-Friday from 8:30AM to 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Diego F. Gutierrez can be reached on 571-272-2245. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2831

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Nguyen T Ha/  
Primary Examiner, Art Unit 2831